



孕龍科技股份有限公司
Zeroplus Technology Co., Ltd.

SPECIFICATION

MODEL: 019-LAP-STBus-M

PART NO : _____

VERSION : V1.05

Approver		Check	Design
GM	PM		

Customer Confirm

* Please fax the file to
Zeroplus Technology after
signing.

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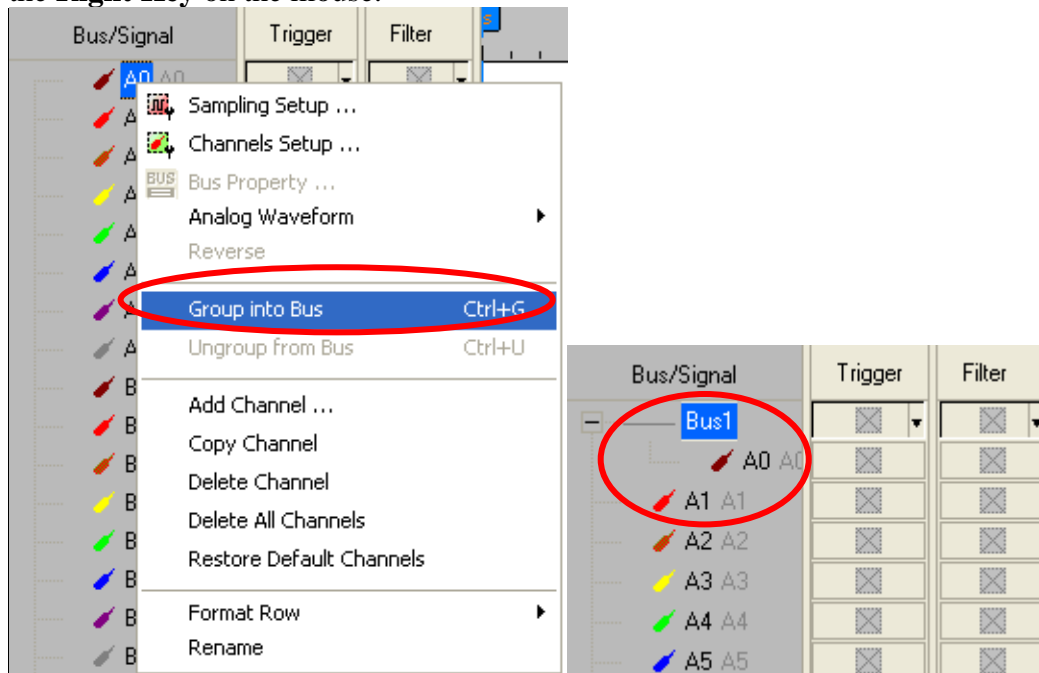
1 Software Register

Please register the software as the following steps:

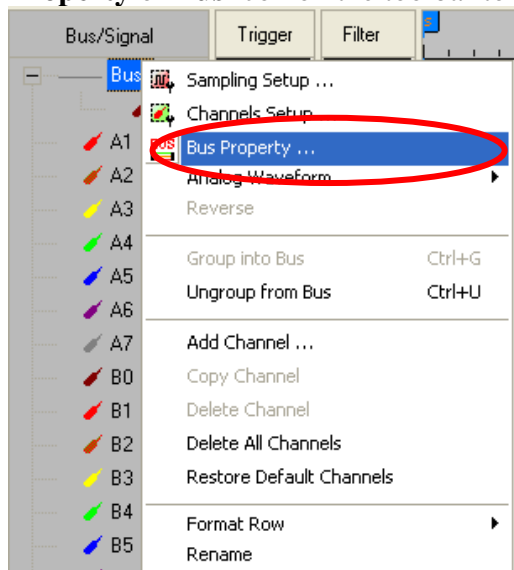
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key** on the mouse.

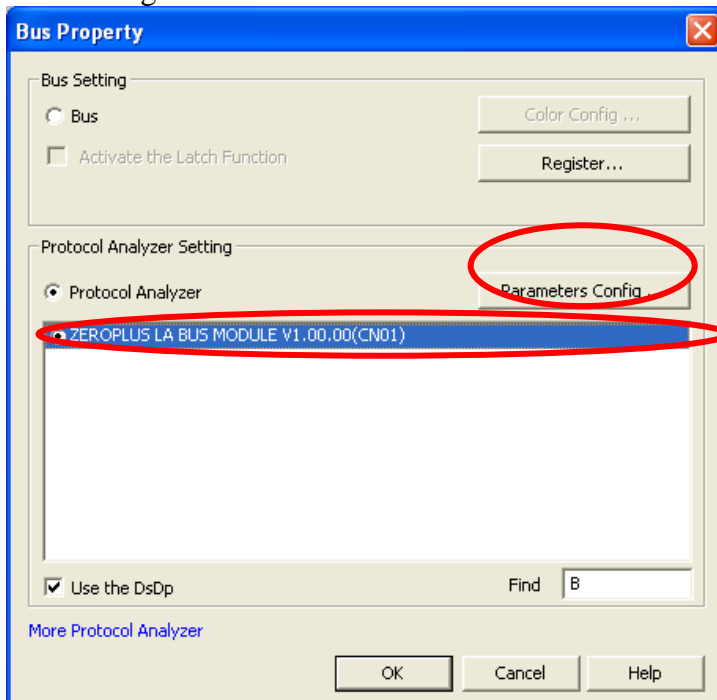


STEP 2. Select **Bus1**, and press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.

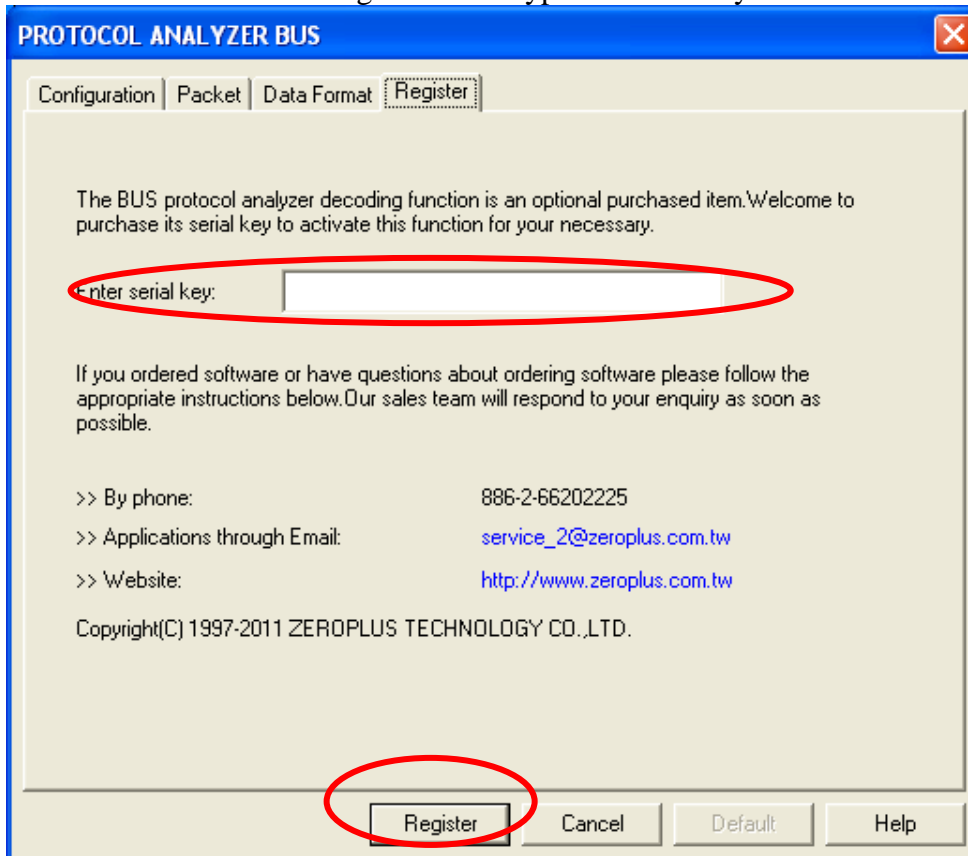




STEP 3. Select Protocol Analyzer, and then choose **ZEROPLUS LA BUS MODULE V1.00.00(CN01)**. Next click **Parameters Configuration** to open the **PROTOCOL ANALYZER BUS** dialog box.

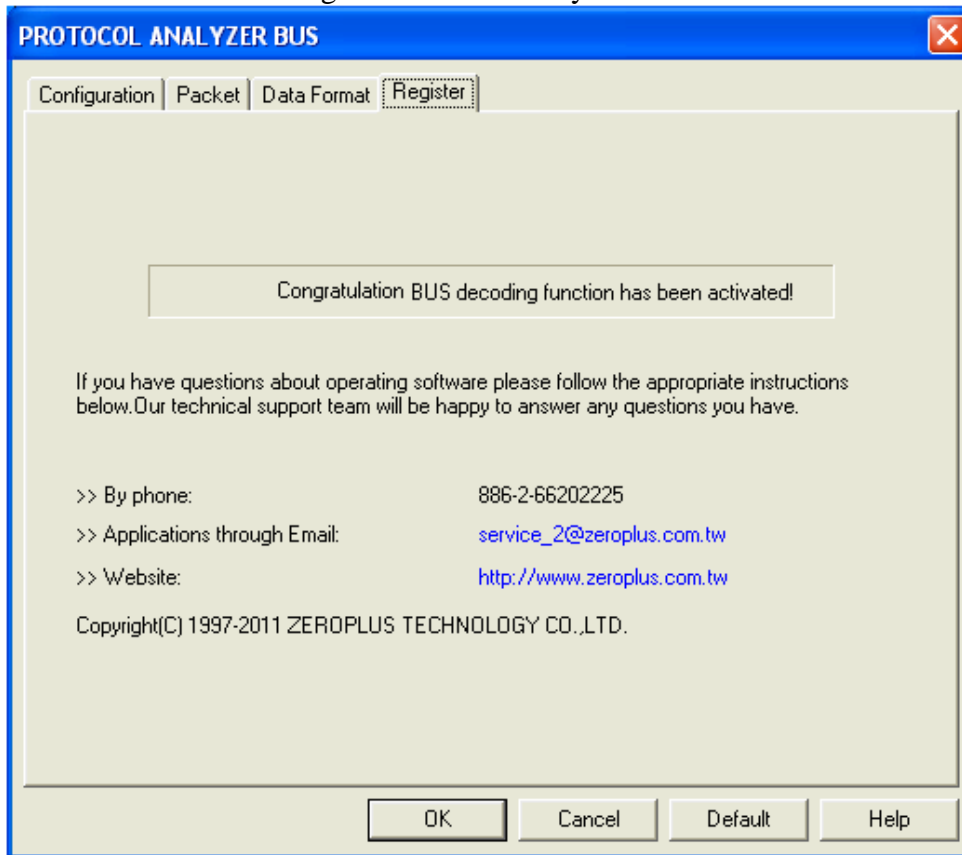


STEP 4. Press the Register tab to type the serial key number of **BUS**. Then, press **Register**.





STEP 5. After pressing the Register button, the following dialog box will appear; it denotes that the BUS has been registered successfully.

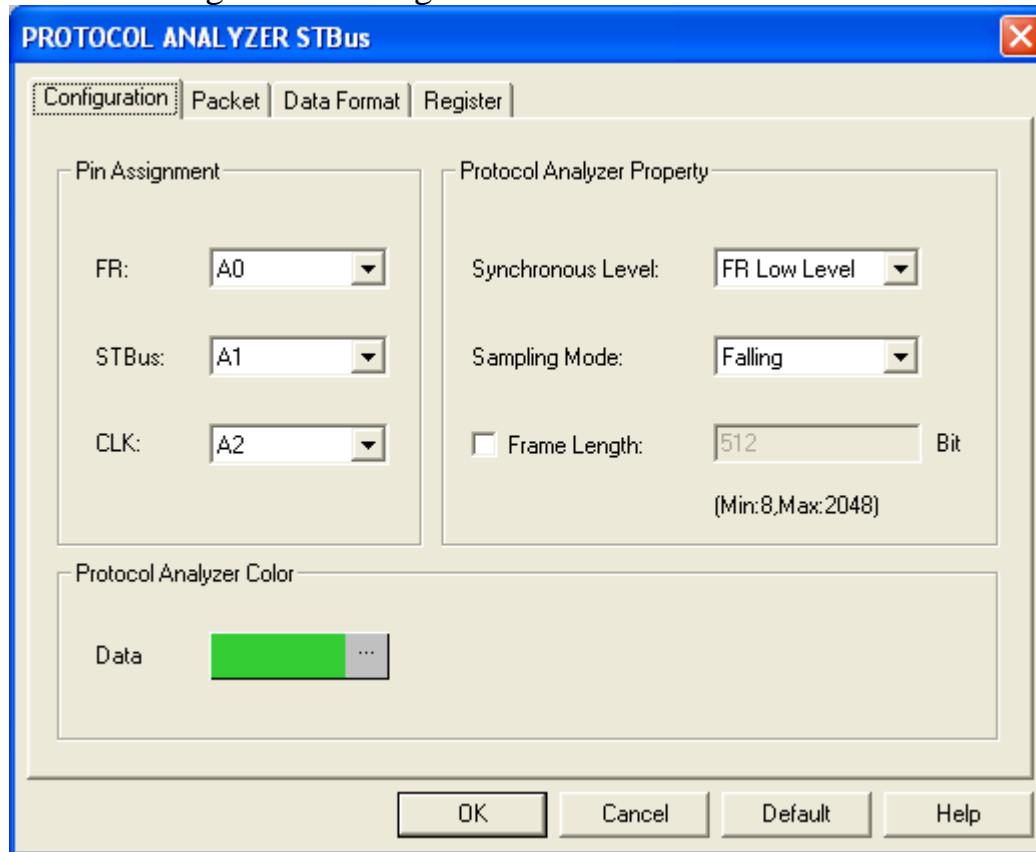




2 User Interface

Please refer to the below images to select options of setting **STBus Module**.

STBus Configuration dialog box



Pin Assignment:

FR: The synchronous signal channel is used to determine the starting and ending positions of the packet. Its default is A0.

STBus: STBus signal line can decode the Protocol Analyzer STBus and be compatible with the Protocol Analyzer GCI Decoding. The default is A1.

CLK: CLK is the frequency signal line. The default is A2.

Protocol Analyzer Property:

Synchronous Level: The options are FR Low Level and FR High Level. The default is FR Low Level.

Sampling Mode:

Falling and **Rising** are listed on the pull-down menu. The default is **Falling**.

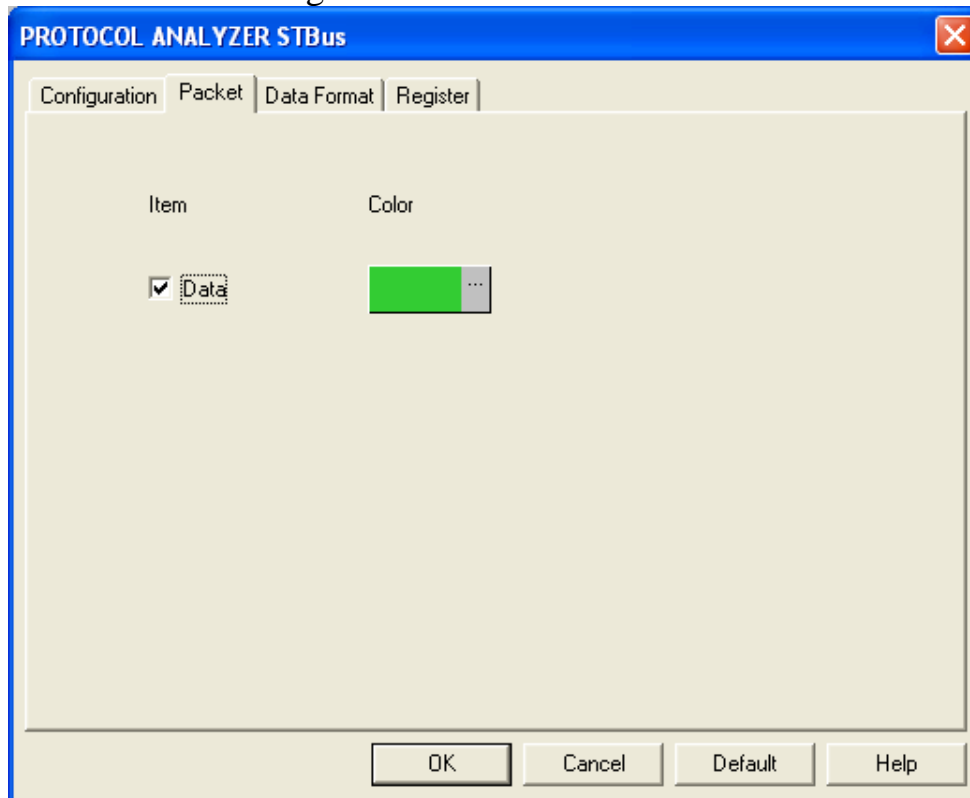
Frame Length: The default is unselected. When the option is selected, the frame length is limited, when the frame length is set as 512 Bit, every packet length is 512 Bits only. The exceed and less frame length will not be decoded. The data is displayed with 8 bits as a group.

Protocol Analyzer Color:

Users can set the colors as their requirements.

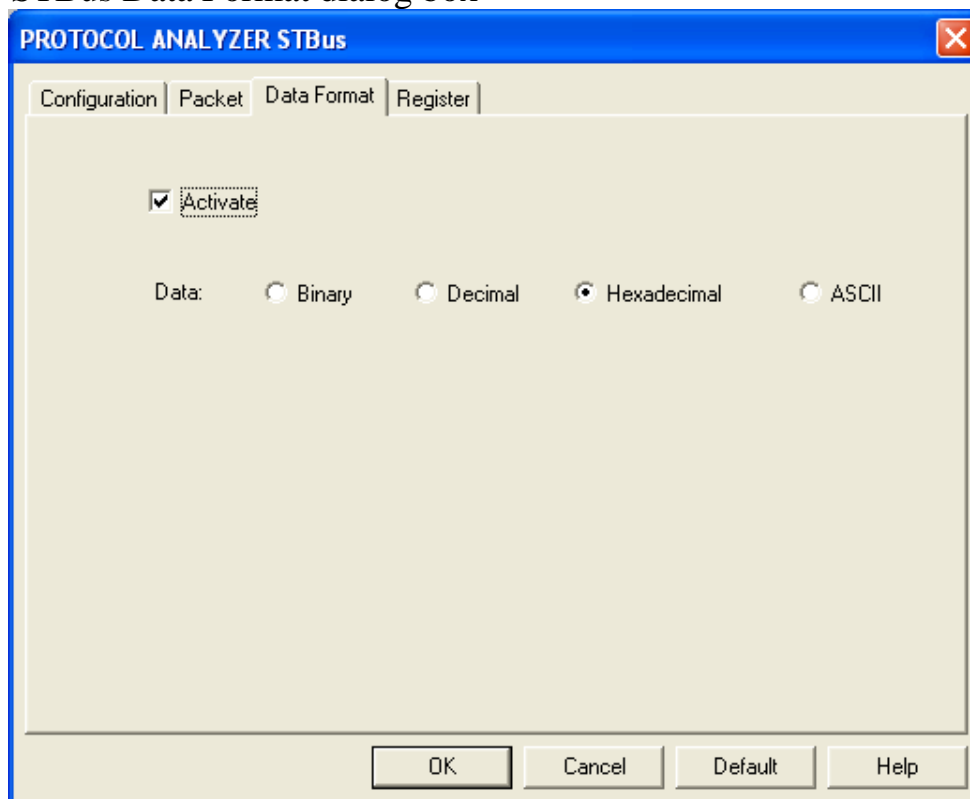


STBus Packet dialog box



In the Packet dialog box, the displayed items and the color configuration can be selected.

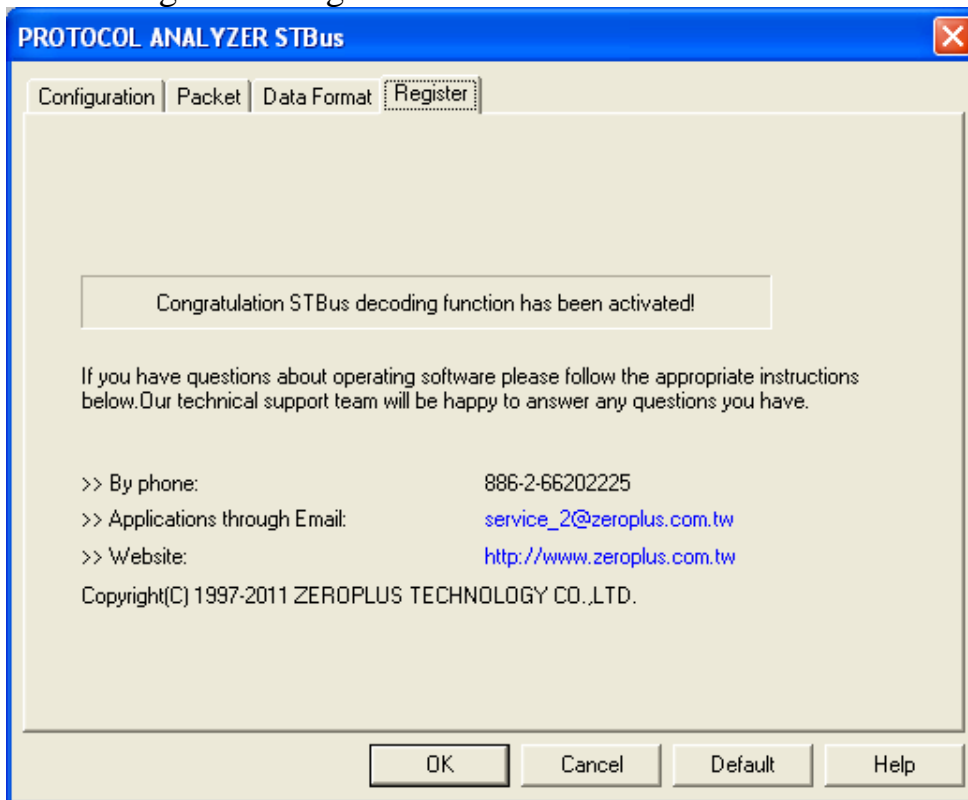
STBus Data Format dialog box





Users can set the Data Format of the Data as their requirements. When selecting the option, Activate, the data format is decided by the settings in the Protocol Analyzer; when not selecting the option, Activate, the data format is decided by the settings in the main program.

STBus Register dialog box

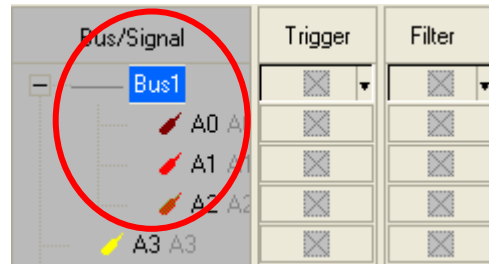
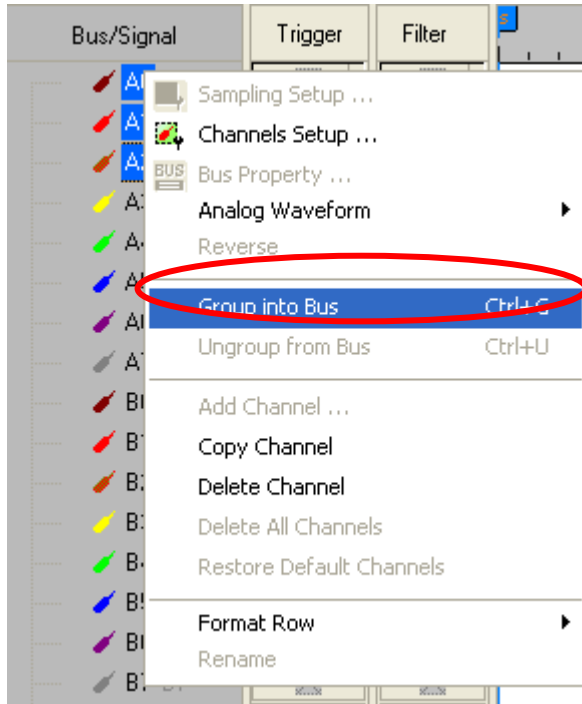


There is ZeroPlus company information written. If users have questions about software operations, they can contact ZeroPlus by Telephone or Email.

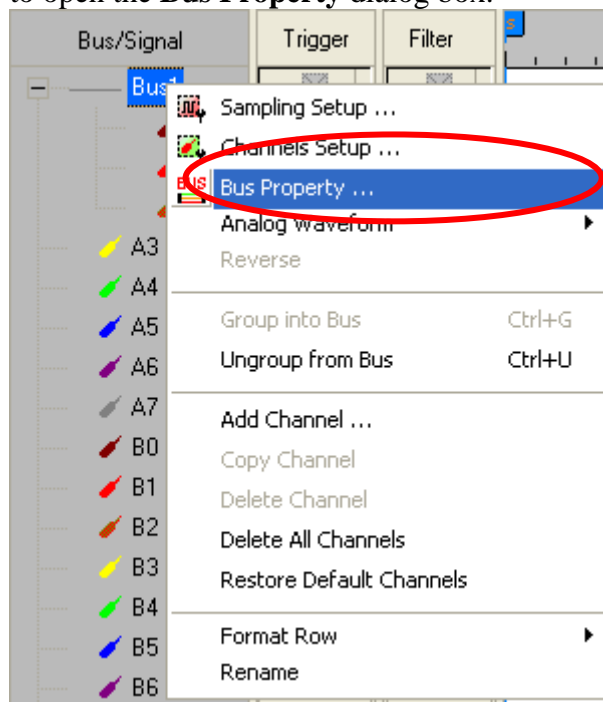


3 Operating Instructions

STEP 1. Group the A0~A2 channels into **Bus1** by pressing the **Right Key** on the mouse. **STBus** needs three channels to decode signals, so it is necessary to group three channels into a Bus at least.

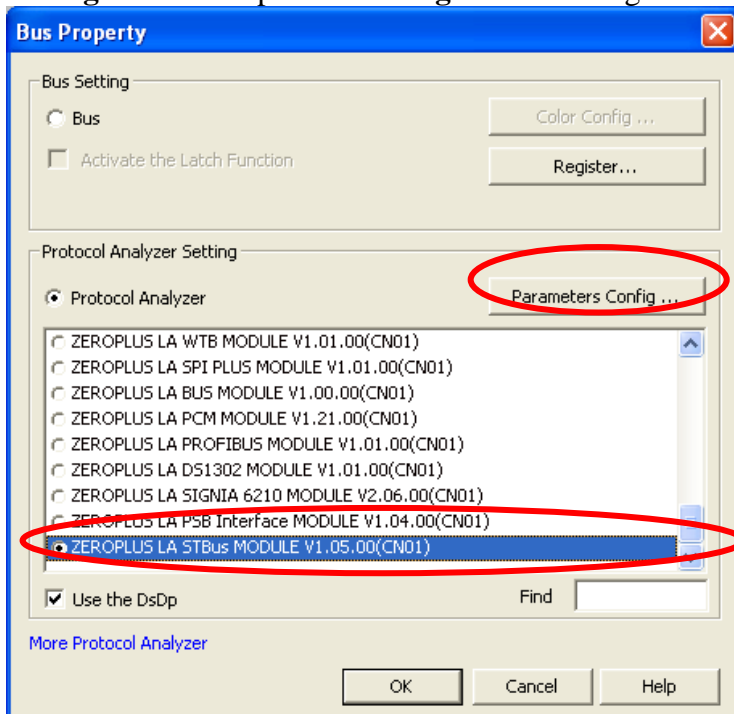


STEP 2. Select **Bus1**, then press **Right Key** on the mouse to list menu. Click **Bus Property** to open the **Bus Property** dialog box.

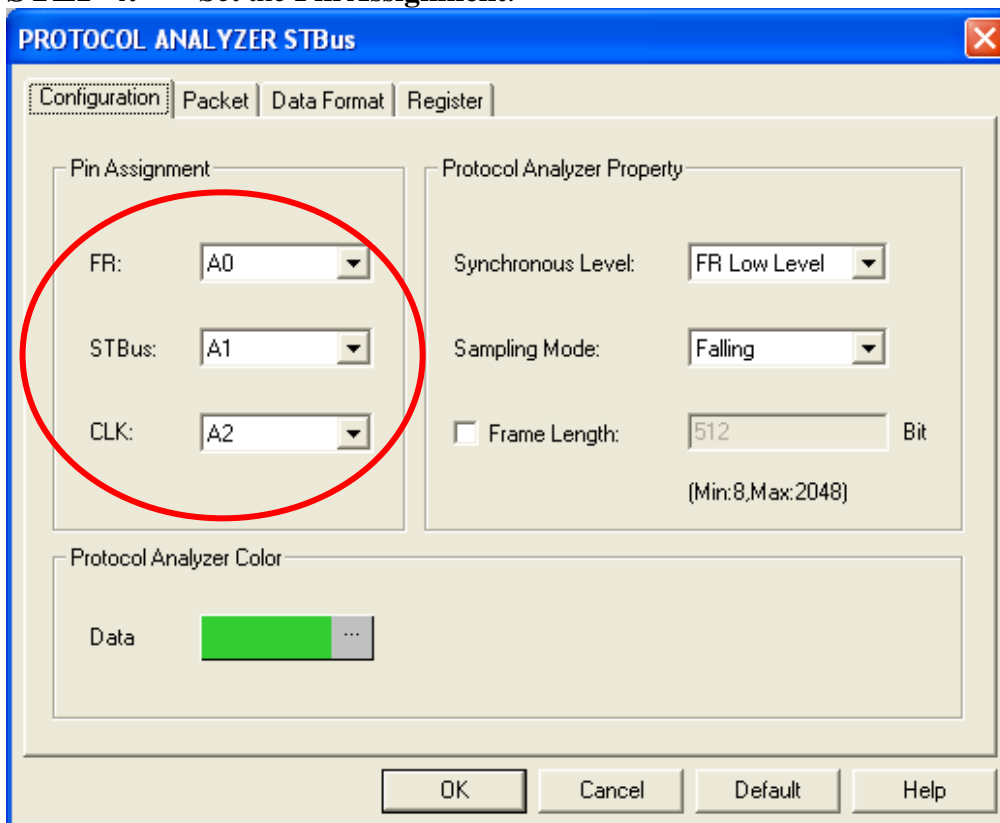




STEP 3. For Protocol Analyzer STBus Parameters Configuration, select Protocol Analyzer, then choose **ZEROPLUS LA STBus MODULE V1.05.01(CN01)**. Next, click **Parameters Configuration** to open the **Configuration** dialog box.

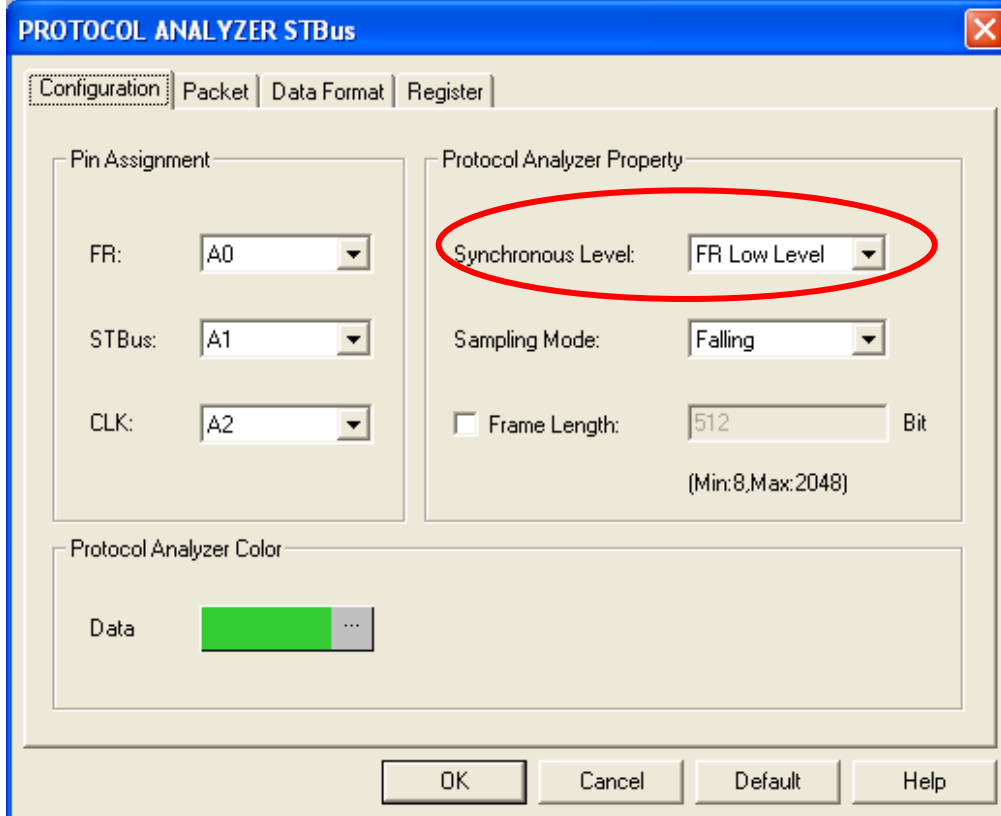


STEP 4. Set the **Pin Assignment**.

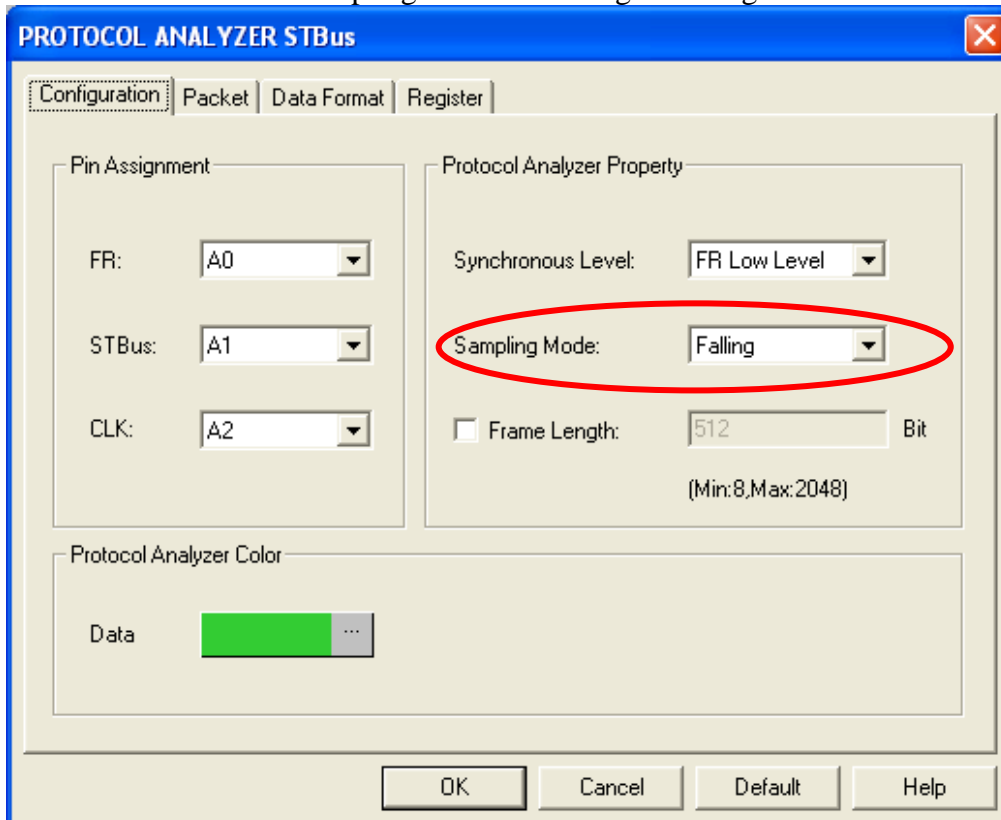




STEP 5. Set the **Synchronous Level** to FR Low Level or FR High Level.

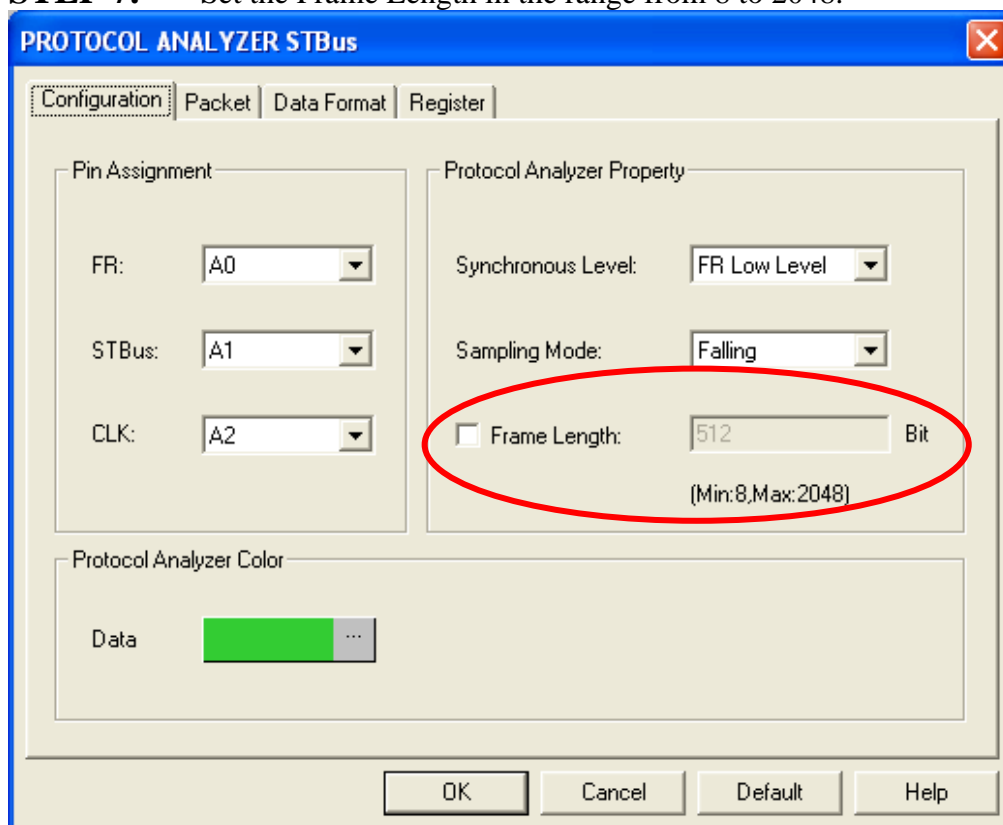


STEP 6. Set the **Sampling Mode** to Falling or Rising.

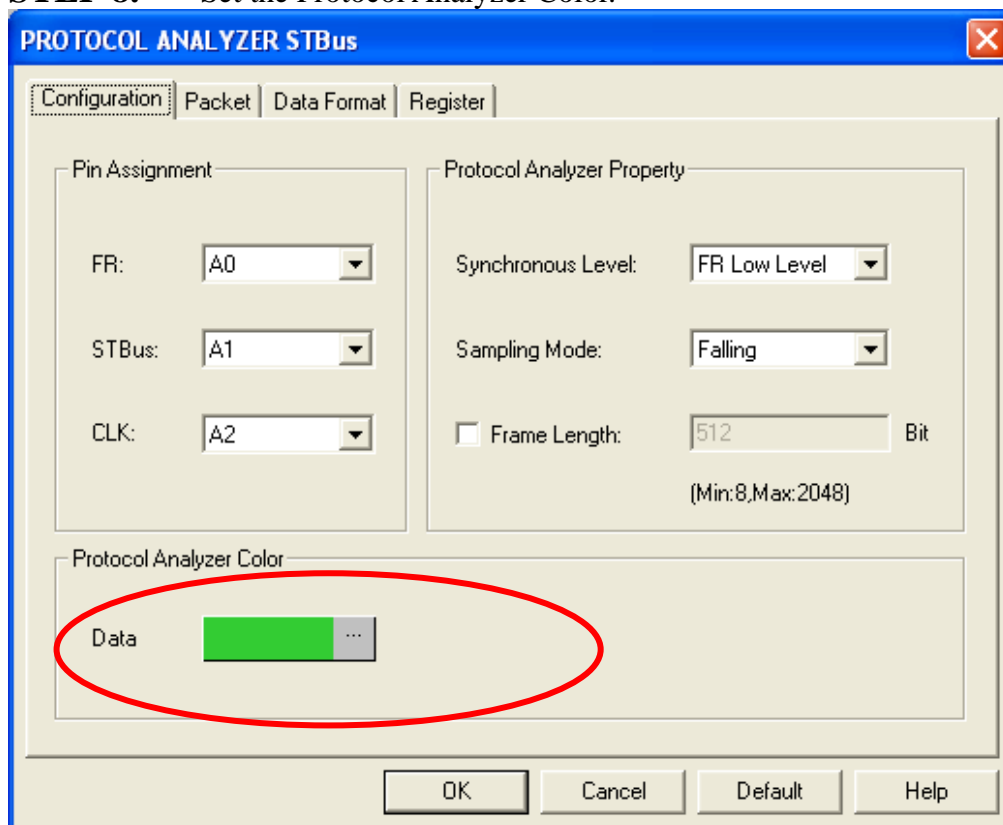




STEP 7. Set the Frame Length in the range from 8 to 2048.



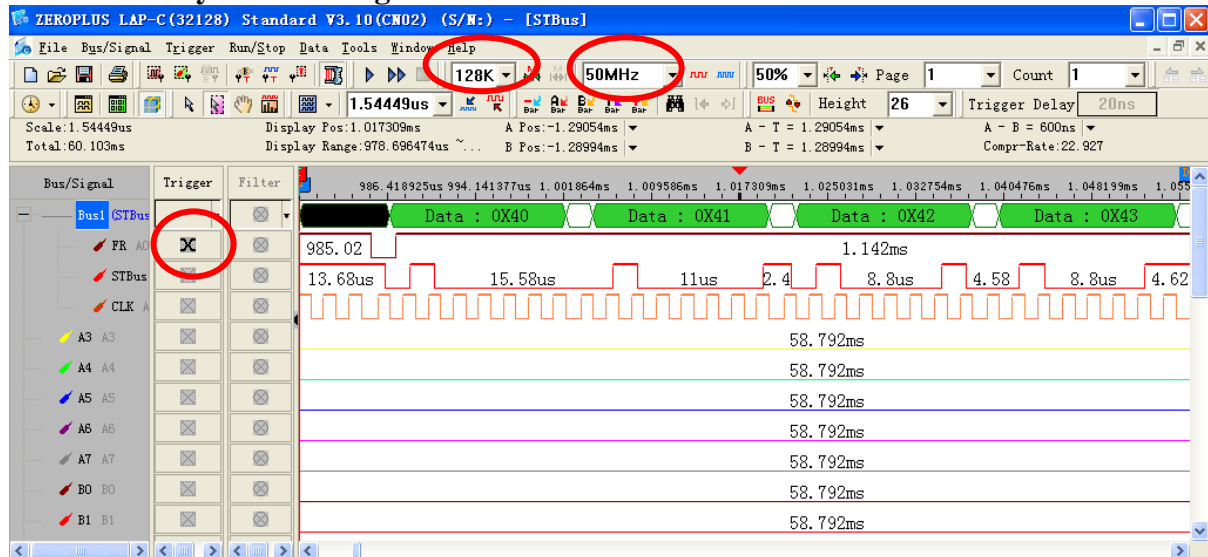
STEP 8. Set the Protocol Analyzer Color.





STEP 9. Following pictures show the completion of the protocol analyzer decoding and the packet list. The trigger condition is set as Rising Edge; the memory depth is 128K; the sampling frequency is 50MHz (the sampling frequency should be more than eight times higher than the signal to be tested).

Protocol Analyzer Decoding



Packet List

